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Sheet 1 of 4

Form 1449\*

Atty. Docket N. : 303.390US3

Serial N. : Unknown

INFORMATION DISCLOSURE STATEMENT  
BY APPLICANT

(Use several sheets if necessary)

Applicant: Joseph E. Geusic et al.

Filing Date: Herewith

Group: Unknown

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
<i>[Signature]</i>	3,968,564	07/13/1976	Springthorpe, A.J.	29	580	04/30/75
<i>[Signature]</i>	4,920,070	04/24/1990	Mukai, R.	437	173	11/27/87
<i>[Signature]</i>	4,970,578	11/13/1990	Tong, E.K., et al.	357	81	09/28/88
<i>[Signature]</i>	5,128,831	07/07/1992	Fox, III, A.C., et al.	361	396	10/31/91
<i>[Signature]</i>	5,221,633	06/22/1993	Holm, P.M., et al.	437	51	09/09/91
<i>[Signature]</i>	5,352,998	10/04/1994	Tamino, N.	333	247	10/01/93
<i>[Signature]</i>	5,362,976	11/08/1994	Suzuki, K.	257	81	10/26/92
<i>[Signature]</i>	5,409,563	04/25/1995	Cathey	156	643	02/26/93
<i>[Signature]</i>	5,489,554	02/06/1996	Gates, J.L.	437	208	02/04/94
<i>[Signature]</i>	5,532,506	07/02/1996	Tserng, H.Q.	257	276	11/14/94
<i>[Signature]</i>	5,604,835	02/18/1997	Nakamura, T., et al.	385	129	12/22/94
<i>[Signature]</i>	5,641,545	06/24/1997	Sandhu, G.S.	427	573	06/07/95
<i>[Signature]</i>	5,656,548	08/12/1997	Zavracky, P.M., et al.	438	23	09/19/95
<i>[Signature]</i>	5,682,062	10/28/1997	Gaul, S.J.	257	686	06/05/95
<i>[Signature]</i>	5,729,038	03/17/1998	Young, W.R., et al.	257	460	12/15/95
<i>[Signature]</i>	5,742,100	04/21/1998	Schroeder, J.A., et al.	257	778	03/27/95
<i>[Signature]</i>	5,760,478	06/02/1998	Rozso, F.M., et al.	257	777	08/20/96
<i>[Signature]</i>	5,767,001	06/16/1998	Bertagnolli, E., et al.	438	455	05/22/94
<i>[Signature]</i>	5,798,297	08/25/1998	Winnerl, J., et al.	438	622	05/02/94
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<i>[Signature]</i>	5,858,814	01/12/1999	Goossen, K.W., et al.	438	107	12/12/96
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<i>[Signature]</i>	04-263462	09/18/1992	Japan	H01L	25/04	
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FOREIGN PATENT DOCUMENTS

**Examiner						Translation
No	Invention Number	Date	Country	Class	Subclass	Yes

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felr	Forbes, L., et al., "Resonant Forward-Biased Guard-Ring Diodes for Suppression of Substrate Noise in Mixed-Mode CMOS Circuits", <u>Electronics Letters</u> , 31, 720-721, (April 1995)
felr	Foster, R., et al., "High Rate Low-Temperature Selective Tungsten", In: <u>Tungsten and Other Refractory Metals for VLSI Applications III</u> , V.A. Wells, ed., Materials Res. Soc., Pittsburgh, PA, 69-72, (1988)
felr	Gong, S., et al., "Techniques for Reducing Switching Noise in High Speed Digital Systems", <u>Proceedings of the 8th Annual IEEE International ASIC Conference and Exhibit</u> , Austin, TX, 21-24, (1995)
felr	Heavens, O., <u>Optical Properties of Thin Solid Films</u> , Dover Pubs. Inc., New York, 155-206, (1965)
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felr	Klaus, et al., "Atomic Layer Controlled Growth of SiO <sub>2</sub> Films Using Binary Reaction Sequence Chemistry", <u>Applied Physics Lett.</u> 70(9), 1092-94, (3 March 1997)
felr	Lehmann, et al., "A Novel Capacitor Technology Based on Porous Silicon", <u>Thin Solid Films</u> 276, Elsevier Science, 138-42, (1996)
felr	Lehmann, V., "The Physics of Macropore Formation in Low Doped n-Type Silicon", <u>Journal of the Electrochemical Society</u> , 140(10), 2836-2843, (Oct. 1993)
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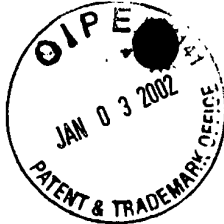
*Julio E. Maldonado*

Date Considered

03/17/03

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gerr	McCredie, B.D., et al., "Modeling, Measurement, and Simulation of Simultaneous Switching Noise", <u>IEEE Transactions on Components, Packaging, and Manufacturing Technology -- Part B</u> , 19, 461-472, (Aug. 1996)
gerr	Muller, K., et al., "Trench Storage Node Technology for Gigabit DRAM Generations", <u>Digest IEEE International Electron Devices Meeting</u> , San Francisco, CA, 507-510, (Dec. 1996)
gerr	Ohba, T., et al., "Evaluation on Selective Deposition of CVD W Films by Measurement of Surface Temperature", <u>In: Tungsten and Other Refractory Metals for VLSI Applications II</u> , Materials Research Society, Pittsburgh, PA, 59-66, (1987)
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gerr	Ramo, S., et al., <u>Fields and Waves in Communication Electronics</u> , Third Edition, John Wiley & Sons, Inc., pp. 428-433, (1994)
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gerr	Stanisic, B.R., et al., "Addressing Noise Decoupling in Mixed-Signal IC's: Power Distribution Design and Cell Customization", <u>IEEE Journal of Solid-State Circuits</u> , 30, 321-326, (Mar. 1995)
gerr	Suntola, T., "Atomic Layer Epitaxy", <u>Handbook of Crystal Growth 3, Thin Films of Epitaxy, Part B: Growth Mechanics and Dynamics</u> , Elsevier, Amsterdam, 601-63, (1994)
gerr	Sze, S.M., <u>VLSI Technology</u> , 2nd Edition, Mc Graw-Hill, NY, 90, (1988)

Examiner <i>Julio F. Maldonado</i>	Date Considered <i>03/17/2002</i>
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OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

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<i>gvd</i>	Vittal, A., et al., "Clock Skew Optimization for Ground Bounce Control", 1996 IEEE/ACM International Conference on Computer-Aided Design, Digest of Technical Papers, San Jose, CA, 395-399, (Nov. 10-14, 1996)
<i>gvd</i>	Wooley, et al., "Experimental Results and Modeling Techniques for Substrate Noise in Mixed Signal Integrated Circuits", IEEE Journal of Solid State Circuits, Vol SC-28, 420-30, (1993)

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